

Code No: **R18A0503****MALLA REDDY COLLEGE OF ENGINEERING & TECHNOLOGY**
(Autonomous Institution – UGC, Govt. of India)**II B.Tech I Semester Supplementary Examinations, January 2024****Data Structures****(CSE)**

Roll No									
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Time: 3 hours**Max. Marks: 70**

Note: This question paper Consists of 5 Sections. Answer **FIVE** Questions, Choosing ONE Question from each SECTION and each Question carries 14 marks.

SECTION-I

- 1 A Write an algorithm to delete an element anywhere from doubly linked list [7M]
 B Define a circular linked list and discuss its advantages and disadvantages [7M]
 compared to linear linked lists.

OR

- 2 A Discuss the operations involved in traversing a singly linked list. Provide [7M]
 algorithms for both iterative and recursive traversals.
 B Explain the processes of searching, insertion, and deletion in a doubly linked [7M]
 list. Provide examples to demonstrate each operation.

SECTION-II

- 3 A Describe the implementation of a stack using a linked list. Compare and [7M]
 contrast this implementation with the array-based approach.
 B Describe how a priority queue differs from a regular queue, and discuss its [7M]
 applications.

OR

- 4 A Explain how heaps can be used to implement a priority queue. Provide [7M]
 examples of operations on a priority queue implemented using heaps.
 B Explain how stacks are used in the evaluation of postfix expressions. [7M]
 Provide a detailed example with the stack operations.

SECTION-III

- 5 A Discuss the basic principles of binary search. Provide a step-by-step [7M]
 explanation and analyze its time complexity.
 B Sort the following list of elements by using Heapsort 35, 48, 20, 80, 22, 36, [7M]
 100,5,15

OR

- 6 A Rearrange following numbers using quick sort: [7M]
 10, 6, 3, 7, 17, 26, 56, 32, 72
 B Discuss the depth-first search (DFS) algorithm for graph traversal. Provide a [7M]
 step-by-step explanation and analyze its time complexity.

SECTION-IV

- 7 A Construct the open hash table and closed hash table for the input: 30, 20, 56, [7M]
 75, 31, 19 using the hash function $h(k) = k \text{ mod } 11$
 B Discuss rehashing as a strategy in hash table management. Explain the [7M]
 conditions under which rehashing is performed and its role in maintaining an

efficient hash table.

OR

8 A Insert the following elements into the hash table by using Quadratic probing [9M]
(size of the hash table 10) 30, 45, 28, 65, 26, 77, 40, 11

B Is linear probing and open addressing same? Justify your answer. [5M]

SECTION-V

9 A What is a binary search tree? Construct a binary search tree given the pre- [7M]
order traversal and in-order traversals as follows: Pre-Order Traversal: G B
Q A C K F P D E R H In-Order Traversal: Q B K C F A G P E D H R

B Compare and contrast B+ Trees with B-Trees in terms of structure, [7M]
operations, and applications.

OR

10 A Explain the steps involved in deleting a node from an AVL Tree. Discuss the [7M]
rotations used to maintain balance and analyze the time complexity of the
deletion operation.

B Define a B-Tree of order m and discuss its properties. Explain how B-Trees [7M]
differ from binary trees and why they are suitable for certain applications.

Code No: **R18A0504****MALLA REDDY COLLEGE OF ENGINEERING & TECHNOLOGY**
(Autonomous Institution – UGC, Govt. of India)**II B.Tech I Semester Supplementary Examinations, January 2024****Operating Systems****(CSE & IT)**

Roll No									

Time: 3 hours**Max. Marks: 70****Note:** This question paper Consists of 5 Sections. Answer **FIVE** Questions, Choosing ONE Question from each SECTION and each Question carries 14 marks.

SECTION-I

1 A Define the concept of an operating system. Discuss its primary functions and why it is essential for computer systems. [7M]

B Discuss the role of the Process Control Block (PCB) in managing processes. Explain the information stored in the PCB. [7M]

OR

2 A Explore the benefits and challenges associated with implementing a virtual machine in an operating system. [7M]

B Explore the various states that a thread can be in during its lifecycle. Discuss the transitions between these states. [7M]

SECTION-II

3 A Define and elaborate on the different types of schedulers in an operating system. Discuss their roles and responsibilities. [7M]

B Explain the First-Come, First-Served (FCFS) scheduling algorithm. Discuss its merits and demerits [7M]

OR

4 A Explain Real-Time scheduling, with a focus on Rate Monotonic (RM) and Earliest Deadline First (EDF) scheduling algorithms. [7M]

B Discuss the concept of a race condition in the context of concurrent processes. Explain the challenges and consequences of race conditions. [7M]

SECTION-III

5 A Define the basic concepts of memory management in an operating system. Discuss the significance of logical and physical address maps. [7M]

B Explain the principle of operation of paging in memory management. Discuss how pages are allocated and the advantages of using paging. [7M]

OR

6 Discuss the optimal page replacement algorithm. Explain its theoretical foundation and how it ensures the most efficient use of virtual memory. [14M]

SECTION-IV

7 Define the concept of a file in the context of file management. Discuss the various attributes that can be associated with a file. [14M]

OR

8 A Explore optimization techniques for file systems, such as caching and indexing, to enhance efficiency and performance. [7M]

B Discuss the concept of I/O buffering and its role in optimizing data transfer [7M]

between devices and the main memory.

SECTION-V

9 *A* Discuss strategies for preventing deadlocks in a system. Explain how each strategy contributes to avoiding the occurrence of deadlocks. **[7M]**

B Explain the Banker's algorithm as a technique for deadlock avoidance. **[7M]**
 Discuss how the Banker's algorithm ensures a safe state and prevents potential deadlocks.

OR

10 *A* Discuss the SSTF disk scheduling algorithm. Explain how it minimizes seek time and its potential drawbacks. **[7M]**

B Discuss advanced disk management techniques. Explain how these techniques enhance data reliability and availability. **[7M]**

Code No: **R18A0505****MALLA REDDY COLLEGE OF ENGINEERING & TECHNOLOGY**

(Autonomous Institution – UGC, Govt. of India)

II B.Tech I Semester Supplementary Examinations, January 2024**Computer Organization****(CSE)**

Roll No									
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Time: 3 hours**Max. Marks: 70**

Note: This question paper Consists of 5 Sections. Answer **FIVE** Questions, Choosing ONE Question from each SECTION and each Question carries 14 marks.

SECTION-I

- 1 **A** A digital computer has a common bus system for 4 registers of 4 bits each. [7M]
 The bus
 constructed with multiplexers:
 i. How many selection lines are there in each multiplexer
 ii. What size of multiplexers are needed
 iii. How many multiplexers are there in the bus? Also draw the bus system
- B** Explain the Error detection and correction codes [7M]
- OR
- 2 **A** Using Booth algorithm perform the multiplication of the signed numbers [7M]
 010111 and 110110. Verify the answer in decimal.
- B** What is system software? What are the various functions performed by [7M]
 system software?

SECTION-II

- 3 **A** Explain the operation of timing and control unit during the execution of an [7M]
 instruction.
- B** Illustrate the computer registers with examples. [7M]
- OR
- 4 **A** Explain arithmetic, logical and shift operations with examples. [7M]
- B** Define and discuss the types of registers. [7M]

SECTION-III

- 5 **A** Briefly explain microprogrammed control organization with neat block [7M]
 diagram.
- B** Explain the difference between hardwired control and micro programmed [7M]
 control. Is it possible to have a hardwired control associated with a control
 memory.
- OR
- 6 **A** What is the significance of Addressing modes? Explain. [7M]
- B** Illustrate the working of a micro program sequencer with the help of [7M]
 diagrams.

SECTION-IV

- 7 **A** Define the Read Only Memory. Explain in detail the types of ROM's. [7M]
- B** Explain the block diagram of Associative memory. [7M]
- OR
- 8 **A** Explain briefly about Associate-mapped and set-associate mapped cache and [7M]

Write a short note on flash memory.

B Difference between the Cache memory-size and block size. [7M]

SECTION-V

9 **A** Discuss the role of I/O devices in the Input-Output Organization of a computer. How are these devices coordinated and managed to ensure efficient data transfer between the CPU and external peripherals? [7M]

B Discuss about the I/O device interfaces. [7M]

OR

10 **A** Explore the role of interrupt handlers in managing interrupts and exceptions. How are these handlers programmed, and what steps are taken to ensure a smooth transition back to the normal program flow after an interrupt or exception is serviced? [7M]

B Write detailed about the x86 architecture. [7M]

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MALLA REDDY COLLEGE OF ENGINEERING & TECHNOLOGY

(Autonomous Institution – UGC, Govt. of India)

II B.Tech I Semester Supplementary Examinations, January 2024

Discrete Mathematics

(CSE & IT)

Roll No									

Time: 3 hours

Max. Marks: 70

Note: This question paper Consists of 5 Sections. Answer FIVE Questions, Choosing ONE Question from each SECTION and each Question carries 14 marks.

SECTION-I

- 1 A Show that $\neg(p \rightarrow q)$ and $(p \wedge \neg q)$ are logically equivalent [7M]
 B Show that t is a valid conclusion from the premises $p \rightarrow q$,
 $q \rightarrow r, r \rightarrow s, \neg s$ and $p \vee t$ [7M]

OR

- 2 A Find the principal Conjunctive normal form of $(\neg p \rightarrow r) \wedge (q \leftrightarrow p)$ [7M]
 B Define Quantifiers, Express the statements into logical expression [7M]
 i) Some student in the class has visited Mexico
 ii) Every student in the class has visited Canada OR Mexico

SECTION-II

- 3 Define Equivalence Relation, prove that the relation R on the set of integers defined by $R = \{(x, y) : x - y \text{ is divisible by } 6\}$ is an Equivalence relation, where x and y are integers [14M]

OR

- 4 A Draw the Hasse diagram for representing the partial ordering $\{(a, b) / a \text{ divides } b\}$ on the set $\{1, 2, 3, 4, 6, 8, 12\}$ [7M]
 B Let $f: z \rightarrow z, g: z \rightarrow z$ defined as $f(x) = 2x + 3, g(x) = 3x + 2$ find [7M]
 i) $f \circ g(x)$ ii) $g \circ f(x)$

SECTION-III

- 5 A Prove that the set $\{0, 1, 2, 3, 4\}$ is a finite group under addition modulo 5 as composition [7M]
 B How many different words can be formed with the letters of the word 'MATHEMATICS'? [7M]

OR

- 6 A Define Homomorphism, show that $f: G \rightarrow G'$ defined as $f(x) = e^x$ is a Homomorphism, where G is a group under addition and G' is a group under multiplication [7M]
 B Find the coefficient of $x^3 y^2 z^4$ in $(x + y + z)^9$ using multinomial theorem [7M]

SECTION-IV

- 7 Solve the recurrence relation $a_n = 3a_{n-1} + 2, a_0 = 1$ by using generating function method [14M]

OR

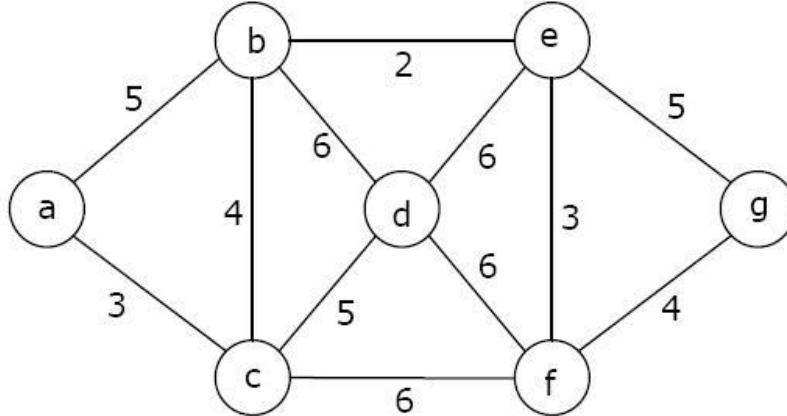
- 8 Solve the recurrence relation $a_n = a_{n-1} + 2a_{n-2}$ with the initial conditions $a_0 = 2, a_1 = 7$ by Characteristics roots method [14M]

SECTION-V

- 9 **A** Define Euler and Hamiltonian graph with an example [7M]
B Show that K_5 is non-planar graph [7M]

OR

- 10 Explain Kruskal's algorithm and hence find a Minimum Spanning Tree [14M]
in the weighted graph shown in figure below



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MALLA REDDY COLLEGE OF ENGINEERING & TECHNOLOGY
(Autonomous Institution – UGC, Govt. of India)

II B.Tech I Semester Supplementary Examinations, January 2024

Probability and Statistics

(CSE & IT)

Roll No									
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Time: 3 hours

Max. Marks: 70

Note: This question paper Consists of 5 Sections. Answer **FIVE** Questions, Choosing ONE Question from each SECTION and each Question carries 14 marks.

SECTION-I

1 **A** A Random variable X has the following Probability function : **[7M]**

X	0	1	2	3	4	5	6	7
P(X)	0	K	2K	2K	3K	K ²	2K ²	7K ² + K

(i) Determine K (ii) Evaluate $P(X < 6)$, $P(X \geq 6)$, $P(0 < X < 5)$ and $P(0 \leq X \leq 4)$ (iii) $P(X \leq K) > 0.5$, find the minimum value of K and, (iv) Determine the distribution function of X (v) Mean (vi) Variance.

B The probability density f(x) of continuous R.V is given by $f(x) = ce^{-|x|}$, $-\infty < x < \infty$. Show that $c = \frac{1}{2}$ and find the mean and variance of the distribution. Also find the probability that the variance lies between 0 & 4. **[7M]**

OR

2 **A** Let X denote the minimum of the two numbers that appear when a pair of fair dice is thrown once. Determine the (i) Discrete probability distribution (ii) Expectation (iii) Variance **[7M]**

B A continuous random variable has the probability density function $f(x) = \begin{cases} kx e^{-\lambda x} ; & x \geq 0; \lambda > 0; \\ 0; & \text{otherwise} \end{cases}$ Determine (i) k (ii) Mean (iii) Variance. **[7M]**

SECTION-II

3 **A** Out of 800 families with 5 children each have. How many would you expect to have (i) 3 boys (ii) 5 girls (iii) either 2 or 3 boys and (iv) atleast one boy **[7M]**

B In a normal distribution, 7% of items are under 35 and 89% are under 63. Determine the mean and variance of the distribution. **[7M]**

OR

4 **A** 2% of the items of a factory are defective. the items are packed in boxes. What is the probability that there will be i) 2 defective items ii) at least three defective items in a box of 100 items. **[7M]**

B Derive mean and variance of a Binomial distribution. **[7M]**

SECTION-III

5 **A** Ten competitors in a musical test were ranked by the three judges A, B&C in the following order **[7M]**

Ranks by A	1	6	5	10	3	2	4	9	7	8
Ranks by B	3	5	8	4	7	10	2	1	6	9
Ranks by C	6	4	9	8	1	2	3	10	5	7

Using rank correlation method, Compare which pair of judges has the nearest approach to common likings in music.

B If $\sigma_x = \sigma_y = \sigma$ and the angle between the regression lines is $Tan^{-1}\left(\frac{4}{3}\right)$. [7M]

Find correlation coefficient r.

OR

6 A Calculate the coefficient of Rank Correlation [7M]

x	68	64	75	50	64	80	75	40	55	64
y	62	58	68	45	81	60	68	48	50	70

B Find the regression line of x on y and y on x for the following data [7M]

X	10	12	13	16	17	20	25
Y	10	22	24	27	29	33	37

SECTION-IV

7 A If the population is 3, 6, 9, 15, 27 i) List all possible samples of size 3 that can be taken without replacement from the finite population. ii) Calculate the mean of each of the sampling distribution of means. iii) Find the standard deviation of sampling distribution of means [7M]

B Write a note on Testing of Hypothesis [7M]

8 A Sample of size 2 are taken from the population 1,2,3,4,5,6 with replacement. Find (a) Mean of the population (b) Standard deviation of the population(c) The mean of the sampling distribution of the means (d) the standard deviation of the sampling distributions of means. [7M]

B Among 100 fish caught in a large lake, 18 were inedible due to the pollution of the environment. With what confidence can we assert that the error of this estimate is at most 0.065? [7M]

SECTION-V

9 A In a random sample of 1000 persons from town A, 400 are found to be consumers of wheat. In a sample of 800 from town B, 400 are found to be consumers of wheat. Do these reveal a significant difference between town A and town B, so far as the proportion of wheat consumers is concerned? [7M]

B Two horses A and B were tested according to the time in seconds to run a particular race with the following results. Test whether you can discriminate between two horses. [7M]

Horse A	28	30	32	33	33	29	34
Horse B	29	30	30	24	27	29	--

OR

10 A Among 900 people in a state 90 are found to be chapatti eaters. Construct 99% confidence interval for the true proportion. [7M]

B The measurements of the output of two units have given the following results. Assuming that both samples have been level whether the two populations have the same variance. [7M]

Unit-A	14.1	10.1	14.7	13.7	
Unit-B	14.0	14.5	13.7	12.7	

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MALLA REDDY COLLEGE OF ENGINEERING & TECHNOLOGY
(Autonomous Institution – UGC, Govt. of India)

II B.Tech I Semester Supplementary Examinations, January 2024

Analog and Digital Electronics

(CSE & IT)

Roll No									
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Time: 3 hours

Max. Marks: 70

Note: This question paper Consists of 5 Sections. Answer FIVE Questions, Choosing ONE Question from each SECTION and each Question carries 14 marks.

SECTION-I

- 1 A Explain V-I characteristics of PN junction diode and compare with ideal diode. [7M]
 B Explain break down mechanisms in semiconductor diodes. [7M]
- OR
- 2 A Define Reverse saturation current of a diode and how it depends on the Temperature during reverse bias. [7M]
 B Explain equivalent circuit of diode. [7M]

SECTION-II

- 3 A Explain the operation of CB Configuration of BJT and its input and output Characteristics briefly. [7M]
 B A transistor with $\alpha=0.97$ has a reverse saturation current of $1\mu\text{A}$ in CB configuration. Calculate the value of leakage current in the CE configuration. Also find the collector current and the emitter current if the value of base current is $20\mu\text{A}$. [7M]

OR

- 4 A Explain working of npn Transistor. [7M]
 B Compare common base, common emitter and common collector configurations. [7M]

SECTION-III

- 5 A Convert the following i) $57.35_{(8)}$ to Binary ii) $57.35_{(8)}$ to Hexadecimal iii) $9\text{BCD}_6_{(16)}$ to Octal [6M]
 B Simplify the following Boolean functions to minimum number of literals: [8M]
 i. $F = X'Y' + XYZ + X'Y$
 ii. $F = X + Y[Z + (X+Z)']$.

OR

- 6 A Distinguish canonical and standard form. Convert Boolean expression in standard form $F=y'+xz'+xyz$ [7M]
 B What are the advantages of 2's complement method ? Perform the subtraction of $10010_2 - 10011_2$ using 2's complement method [7M]

SECTION-IV

- 7 A Simplify using K map $F(A,B,C,D)=\Sigma m(7,8,9)+d(10,11,12,13,14,15)$ [7M]
 B Simplify $A'C'D' + AC' + BCD + A'CD' + A'BC + AB'C'$ [7M]

OR

- 8 A Simplify the following Boolean expressions using K-map and implement [7M]

them using NOR gates:

$$F(A, B, C, D) = AB'C' + AC + A'CD'$$

- B** Determine Prime implicants and essential prime implicants for the following function. [7M]

$$F(w,x,y,z) = \sum m(8,12,13,18,19,21,22,24,25,28,30,31) + d(1,2,4,6,7,11,26)$$

SECTION-V

- 9** **A** Explain the operation of full adder with neat diagram [7M]
B With neat diagram explain the operation of JK Flip-flop .How Race-around Problem is eliminated [7M]

OR

- 10** **A** Design BCD to Gray code converter and realize using logic gates [7M]
B Explain the operation of Multiplexer with neat diagram [7M]
